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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Patent Application of : Group Art Unit: Not Yet Assigned
Yoshimasa OKABE *et al.* :
: Conf. No.: Not Yet Assigned :
: International :
Appln. No.: PCT/JP01/03376 : Examiner:
: International 19 April 2001 :
Filing Date: (19.04.01) :
: Attorney Docket
For: ELECTRONIC APPARATUS AND : No.: 8861-417US
PRODUCTION METHOD FOR THE SAME : (P25168-02)

PRELIMINARY AMENDMENT

Simultaneously with the filing of the above-identified application with which this Preliminary Amendment is being filed, and prior to the calculation of the filing fee, Applicant hereby amends the application as follows, without prejudice:

In the Claims:

Please amend the claims as follows:

Please amend claims 19 and 25 to read as follows. A marked-up copy of the amended claims is attached hereto, having the bracketed additions and stricken deletions.

--19. A production method for an electronic apparatus according to claim 1, wherein said central processing unit is an IEEE 1149 standard complaint device.

25. An electronic apparatus according to claim 20, wherein said central processing unit is an IEEE 1149 standard complaint device.--

REMARKS

Claims 1 to 25 are pending in the application.

The purpose of this amendment is to place the claims in appropriate U.S. form

and delete the multiple dependent claims in this application, and thereby eliminate excessive claim fees. Such amendments are formal in nature and no new matter is added by any of the above amendments. A marked-up copy of the amended claims is enclosed to reflect these amendments. Entry of this amendment and early examination of this application are respectfully solicited.

Respectfully submitted,

YOSHIMASA OKABE *et al.*

12/20/01

(Date)

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Enclosure

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Marked up Copy of Claims

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18. A production method for an electronic apparatus according to claim 10, wherein said board is a multi-layer board consisting of at least four layers, and at least one connecting line connecting between said central processing unit and said relaying device is formed in an inner layer of said board.

19. A production method for an electronic apparatus according to ~~any one of claims 1 to 18~~, wherein said central processing unit is an IEEE 1149 standard compliant device.

20. An electronic apparatus comprising a board having a separable region, said board being mounted with a central processing unit, an electrically alterable nonvolatile storage device, and a connector mounted on said region, wherein

when said region is not separated yet, data can be written to said storage device by connecting an external apparatus to said connector and directly controlling an internal logic circuit of said central processing unit, and

when said region is separated, data cannot be written to said storage device by controlling said central processing unit.

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descrambled data can be transferred to said central processing unit, and by connecting said external apparatus to said connector and directly controlling the internal logic circuit of said central processing unit, said central processing unit can be caused to output data to said scrambling device and said scrambling device can scramble the output data of said central processing unit with a second scrambling pattern, a scrambling pattern different from said first scrambling pattern, and write the scrambled data to said storage device; and

when said first region and said second region are both separated, data read out of said storage device can be descrambled with said second scrambling pattern by said scrambling device and the descrambled data can be transferred to said central processing unit, but data cannot be written to said storage device via said scrambling device by directly controlling the internal logic circuit of said central processing unit through said external apparatus connected to said connector.

25. An electronic apparatus according to ~~any one of claims 20 to 24~~, wherein said central processing unit is an IEEE 1149 standard compliant device.

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